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## BIB DATA SHEET

CONFIRMATION NO. 5852

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.	
10/612,033	07/03/2003 RULE	257	2826	03180.0326	
<b>APPLICANTS</b> Tadashi Iguchi, Mie-ken, JAPAN; Katsuhiro Ishida, Mie-ken, JAPAN; Hiroaki Tsunoda, Mie-ken, JAPAN; Hirohisa Iizuka, Mie-ken, JAPAN; Hiroaki Hazama, Tokyo, JAPAN; Seichi Mori, Tokyo, JAPAN; <input checked="" type="checkbox"/> /KQ/					
<b>** CONTINUING DATA *****</b> <b>** FOREIGN APPLICATIONS *****</b> JAPAN P2002-338452 11/21/2002 <input checked="" type="checkbox"/> /KQ/					
<b>** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **</b> 09/26/2003					
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/KEVIN QUINTO/</u> Examiner's Signature	<input type="checkbox"/> Met after Allowance Initials	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWINGS</b> 13	<b>TOTAL CLAIMS</b> 17	<b>INDEPENDENT CLAIMS</b> 3
<b>ADDRESS</b> FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER LLP 901 NEW YORK AVENUE, NW WASHINGTON, DC 20001-4413 UNITED STATES					
<b>TITLE</b> SEMICONDUCTOR MEMORY PREVENTING AN ELECTRIC SHORT CIRCUIT BETWEEN A WORD LINE AND A SEMICONDUCTOR SUBSTRATE, AND MANUFACTURING METHOD FOR THE SEMICONDUCTOR MEMORY					
<b>FILING FEE RECEIVED</b> 1224	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		